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**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Haruo NISHIDA et al.

Group Art Unit: 2131

Application No.: 10/765,895

Examiner: A. SHEIKH

Filed: January 29, 2004

Docket No.: 118496

For: TEST CIRCUIT, INTEGRATED CIRCUIT, AND TEST METHOD

**SECOND STATUS INQUIRY LETTER**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please telephone our firm's Patent Application Filing Department at 703-836-6400 with the status of the above-identified patent application, including an indication as to when the next communication can be expected.

Respectfully submitted,

James A. Oliff  
Registration No. 27,075

Linda M. Saltiel  
Registration No. 51,122

JAO:LMS/crh

Date: January 26, 2006

**OLIFF & BERRIDGE, PLC**  
**P.O. Box 19928**  
**Alexandria, Virginia 22320**  
**Telephone: (703) 836-6400**